Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S4	0	difference with local adj bus with memory adj bus	USPAT	OR	OFF	2005/05/13 12:18
S5	1946	local adj bus with memory adj bus	USPAT	OR	OFF	2005/05/04 14:42
S6	32	(local adj bus with memory adj bus).clm.	USPAT	OR	OFF	2005/05/04 14:42
S7	27	(local adj bus with memory adj bus).clm. and g06f$.ipc.	USPAT	OR	OFF	2005/05/04 15:37
S8	51	first adj memory adj bus same second adj memory adj bus	USPAT	OR	OFF	2005/05/04 15:43
S9	0	first adj memory adj bus same second adj memory adj bus same (power speed)	USPAT	OR	OFF	2005/05/04 15:38
S10	0	first adj memory adj bus same second adj memory adj bus and (power adj mode)	USPAT	OR	OFF	2005/05/04 15:39
S11	2	first adj memory adj bus same second adj memory adj bus and (clock adj speed)	USPAT	OR	OFF	2005/05/04 15:39
S12	10	first adj memory adj bus same second adj memory adj bus and (power near2 (conserv$5 sav$3 reduc$4 consum$5))	USPAT	OR	OFF	2005/05/04 15:47
S13	0	first adj memory adj bus same second adj memory adj bus same local adj bus	USPAT	OR	OFF	2005/05/04 15:47
S14	0	first adj memory adj bus same second adj memory adj bus same common adj bus	USPAT	OR	OFF	2005/05/04 15:48
S15	16	first adj memory adj bus same second adj memory adj bus and (local common) adj bus	USPAT	OR	OFF	2005/05/04 15:48
S16	72448	rom with ram	USPAT	OR	OFF	2005/05/13 12:18
S17	768	rom with ram with (separate independent)	USPAT	OR	OFF	2005/05/13 12:18
S18	86	rom with ram with (separate independent) with bus$2	USPAT	OR	OFF	2005/05/13 12:19
S19	11	rom with ram with (separate independent) adj bus$2	USPAT	OR	OFF	2005/05/13 12:22
S20	2	rom adj bus$2 with ram adj bus$2 with (separate independent)	USPAT	OR	OFF	2005/05/13 12:22